

Shri Vaishnav Vidyapeeth Vishwavidyalaya, Indore (MP)

Choice Based Credit System (CBCS) Scheme

B.Tech in Electronics and Communication - IOT

II - SEMESTER

Sr. No.	Subject Code	Name of Subject	Course Code	Teaching Scheme/ Week			Examination Scheme					Total Marks	Credits
				L	T	P	Theory			Practical			
							End Sem University Exam	Two Term Exam	Teachers Asses-ment*	End Sem University Exam	Teachers Asses-ment*		
1	BTCH101	Applied Chemistry		3	1	2	60	20	20	30	20	150	5
2	BTMA201	Applied Mathematics-II		3	1	0	60	20	20	0	0	100	4
3	BTCE103	Applied Mechanics		3	1	2	60	20	20	30	20	150	5
4	BTEC101	Introduction to Communication Engineering		3	1	2	60	20	20	30	20	150	5
5	BTME102	Fundamental of Mechanical Engg.		3	1	2	60	20	20	30	20	150	5
6	BTEC103	Electronics Workshop		0	0	2	0	0	0	30	20	50	1
7	BTME103	Workshop Practices		0	0	2	0	0	0	30	20	50	1
TOTAL				15	5	12	300	100	100	180	120	800	26

*Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Paticipation in Class, given that no component shall exceed more than 10 marks.



**Chairperson
Board of Studies**

Shri Vaishnav Vidyapeeth Vishwavidyalaya
Indore



**Joint Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore**